

VE3LNY-PACKET ADAPTER BOARD  
Clock circuit by VE3DVF.

AUG/84.

ADDITIONAL CIRCUITRY FOUND  
IN DOCUMENTATION → IBM TECK REF.  
AND PROTOTYPE MANUAL.

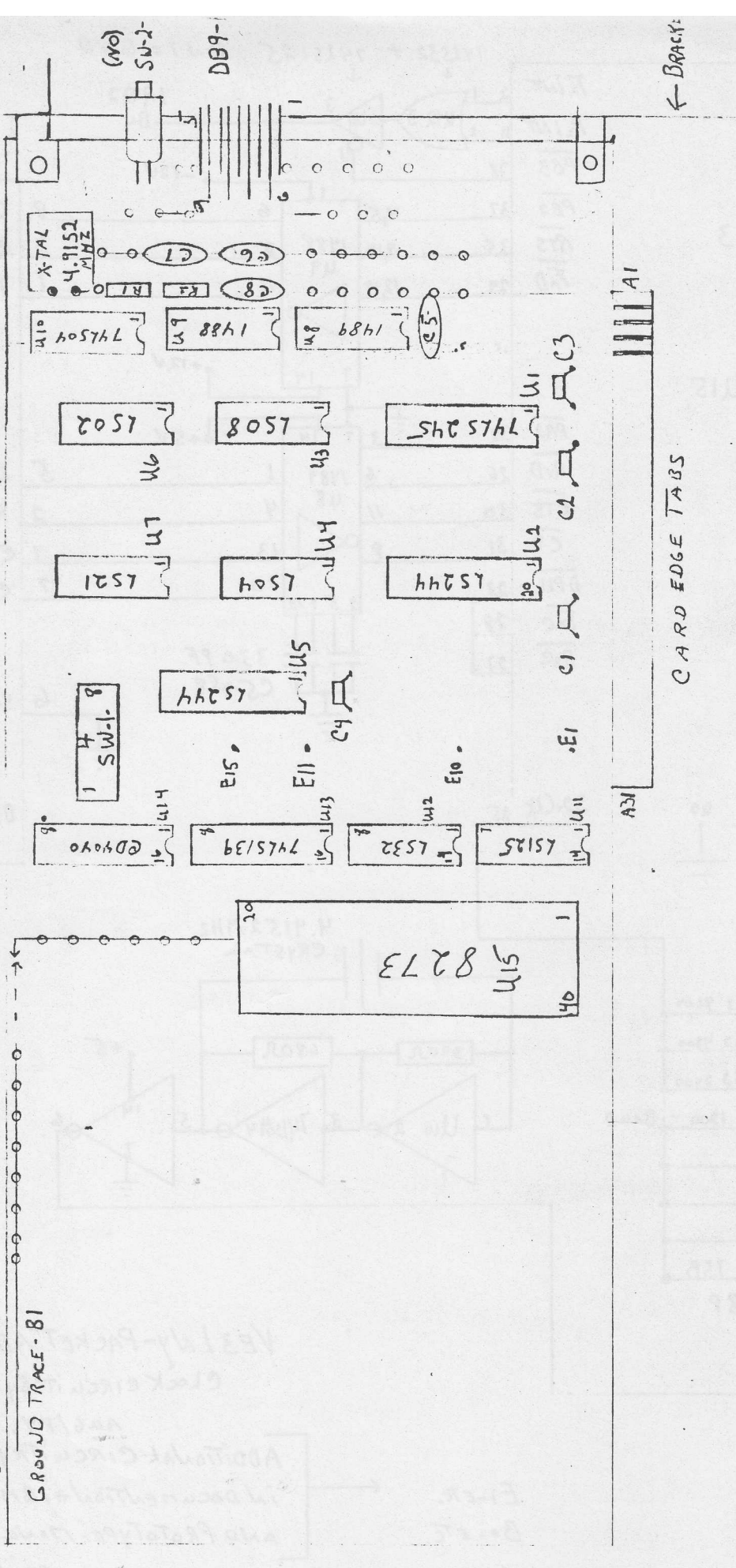
DRAWING -06-09-8  
VE3LNU

E1-ETC.  
B0-ETC.

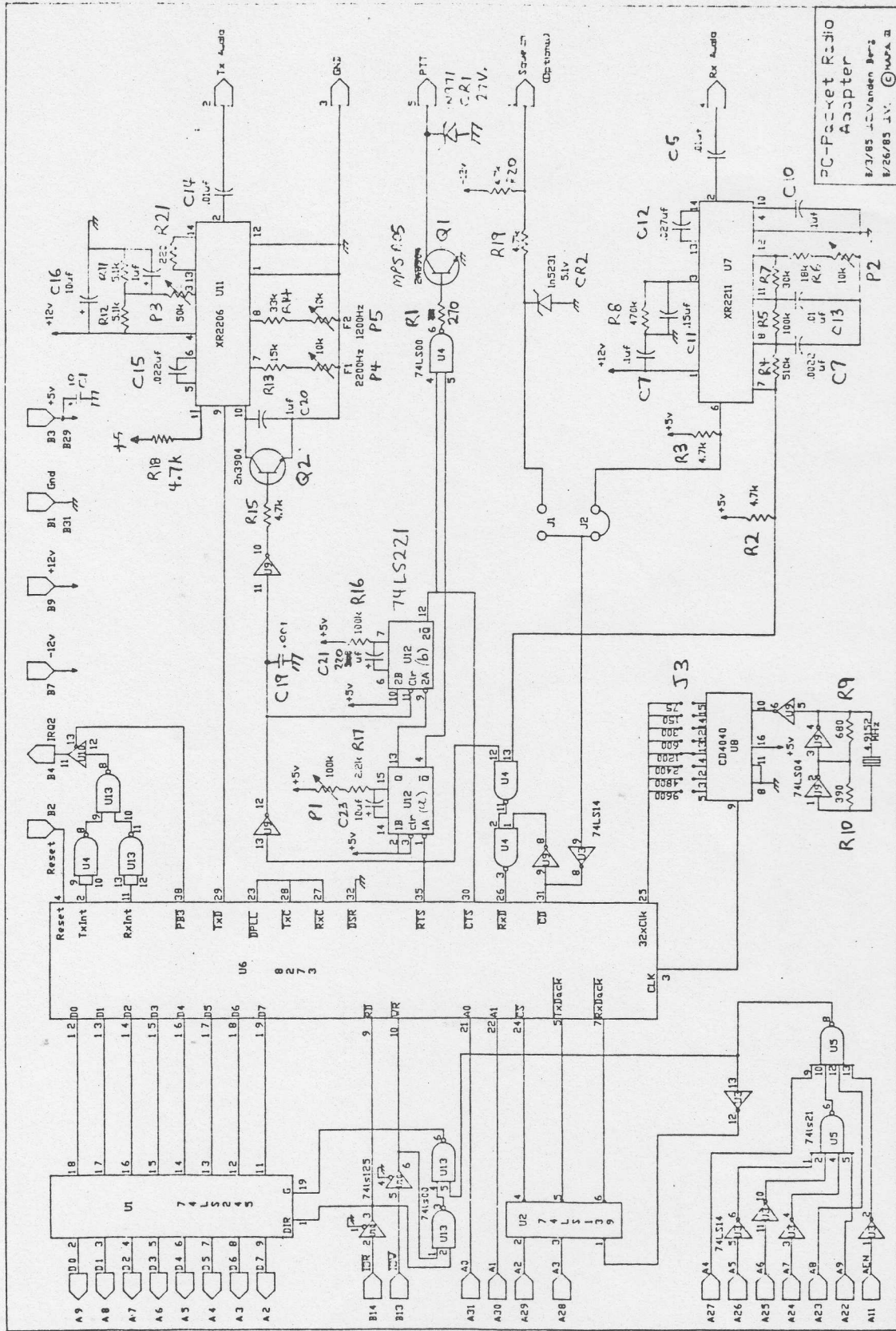
BOARD CONTINUES.  
BLANK SPACE

IBM PROTOTYPE CARD

WITH JACK BOTNER'S TNC ADAPTER (VE3LWY)  
CLOCK CIRCUIT MODIFIED BY JOHN VANDENBERG (VE3DUU)



NOTE: U4 PIN 7 TO GROUND  
TRACE MISSING ON CARD

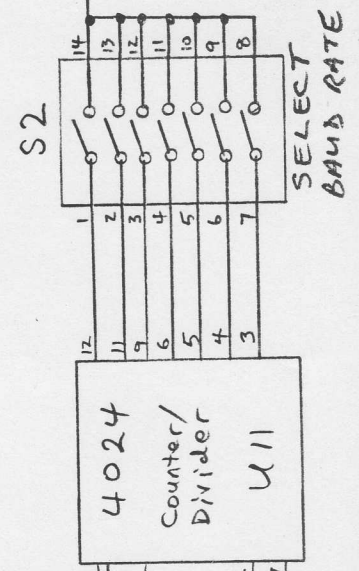
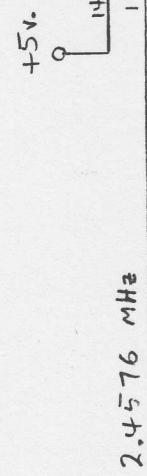
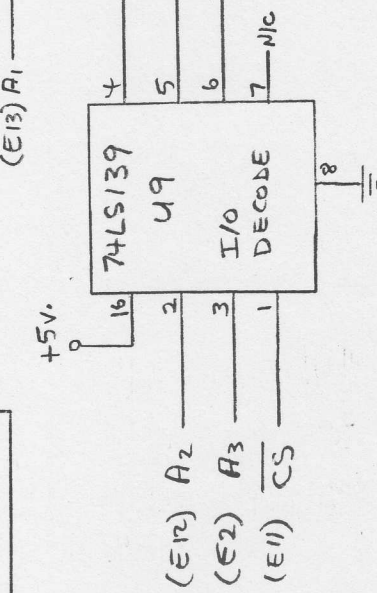
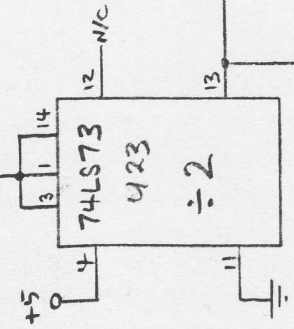
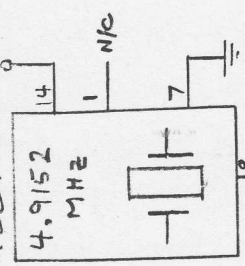


C2, C3, C4, C6, C8, C17, C18, C22 (Not shown) bypass for supply of I.C.s (0.1µf)

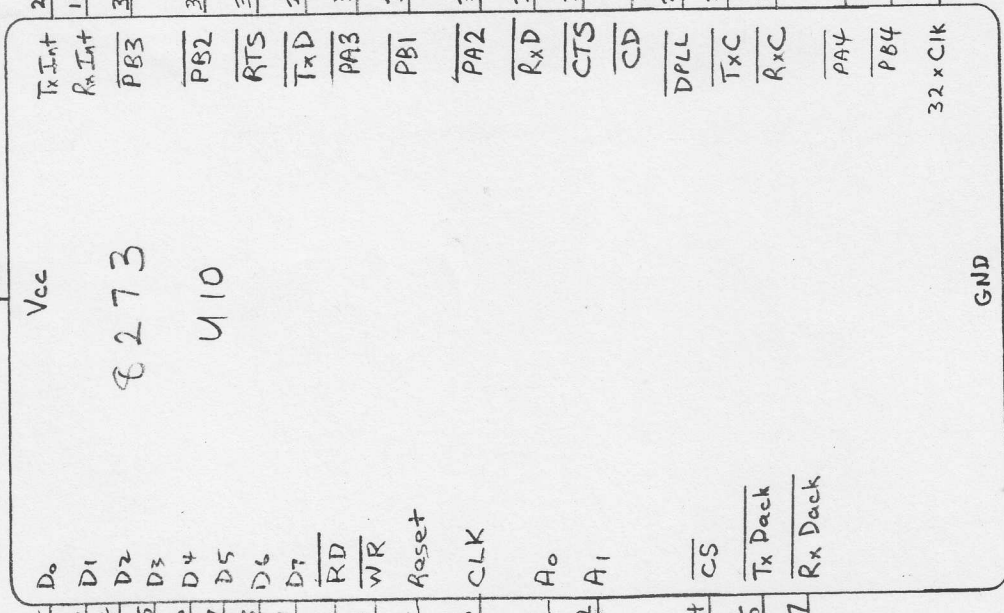
QC-Packet Radio Adapter  
E/3/85 J.C. van den Berg  
E/26/85 J.V. © WPA 2



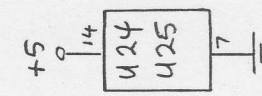
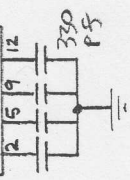
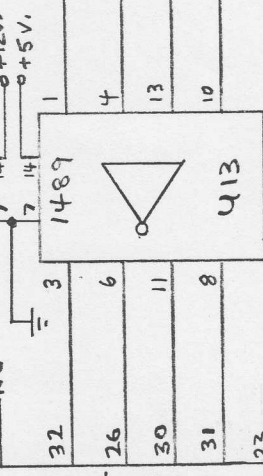
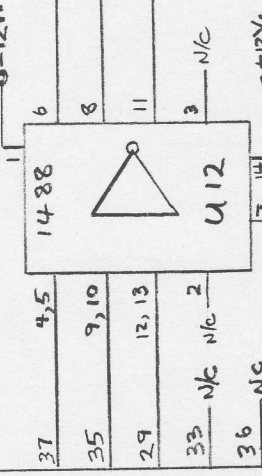
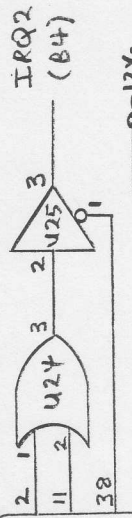
HYBRID  
OSCILLATOR +5v.



+5v.



74LS32 74LS125



8273 Adapter for IBM-PC  
by VE3LNY 09/83  
uses I/O decoding on  
prototype board.